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RA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/032,863 03/02/98 GRIGOR G 0100.01117

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EXAMINER

NGUYEN, K

ART UNIT

PAPER NUMBER

2774

13

DATE MAILED:

09/25/00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

RA

Office Action Summary

Application No.

09/032,863

Applicant(s)

GRIGOR ET AL.

Examiner

Kevin M. Nguyen

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2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Status

- 1) ☒ Responsive to communication(s) filed on 31 August 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 14, 17-19, 21-24, 29-33 and 38-48 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 14, 17-19, 21-24, 29-33 and 38-48 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some * c) ☐ None of the CERTIFIED copies of the priority documents have been:
1. ☐ received.
2. ☐ received in Application No. (Series Code / Serial Number) _____.
3. ☐ received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. & 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892)
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 18) ☐ Interview Summary (PTO-413) Paper No(s) _____
- 19) ☐ Notice of Informal Patent Application (PTO-152)
- 20) ☐ Other: _____

DETAILED ACTION

Continued Prosecution Application

1. The request filed on 8/31/2000 for a Continued Prosecution Application (CPA) under 37 CFR 1.53(d) based on parent Application No. 09/032,863 is acceptable and a CPA has been established. An action on the CPA follows.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

3. Claims 14, 17-19, 21-24, 29-33 and 38-48 are rejected under 35 U.S.C. 102(e) as being anticipated by Nolan et al. (U.S. Patent No. 6,049,316).

As to claims 14, 17-19, 21-24, 29-33 and 38-48, Nolan et al. teaches a video graphics processing circuit 50 as show in figure 5 and 6 (column 7, line 10) which includes the display driver is typically produced by the manufacturer of the graphics chip or card. Different display drivers are used for different operating system (column 10, lines 44-46), a LCD controller 62 to drive display LCD 22 (column 7, lines 59-61), and Vsync timer 76, driver 77, DAC 58 to drive display CRT 24 (column 8, lines 11-13).

During a screen refresh, all the pixels displayed on the screen are fetched from video memory 60 and serially transferred to flat-panel display 22 or external CRT 24 (column 7, lines 43-45).

Host interface 70 also receives commands from software such as display drivers executing on the PC's microprocessor. Host interface 70 receives read and write commands and write data and reads or writes internal configuration registers on the graphics controller chip. These configuration registers include CRT control registers 68 which specifies the horizontal and vertical refresh or synchronization rates, and the number of pixels per line. Panel registers 64 contain additional panel-specific configuration data, such as frame clock rate, blanking, and half-frame configurations (column 8, lines 1-10). Host interface 70 corresponds to coupling controller as claimed.

A portable personal computer (PC) can be connected to a variety of different external CRT monitors. Configuration of each CRT monitor is performed by the graphics display driver software so that the user does not have to re-configure the graphics sub-system every time a different CRT monitor is connected. Auto-configuration of Plug-and-Play monitors occurs by reading configuration information from the monitor itself. For Windows 95, the Plug-and-Play drivers are used for auto-configuration, or for older operating systems the video BIOS display-data-channel functions is used. Older "legacy" CRT monitors that do not support Plug-and-Play are still auto-configured. The vertical refresh rate for each resolution is stored in a default register on the graphics controller chip. The vertical refresh rate from default register is copied to an active refresh-rate register when a legacy (non Plug-and-Play) monitor is detected. When a Plug-and-Play monitor is detected, the refresh rate is calculated and written to the active refresh-rate register, without overwriting the default registers. Thus a Plug-and-Play monitor can be connected without losing configuration information for the legacy monitors. A Plug-and-Play

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flag is also added to the graphics controller chip to disable the video BIOS auto-configuration when Windows 95 is used (abstract).

Response to Arguments

4. Applicant's arguments with respect to claims 14, 17-19, 21-24, 29-33, 38-48 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO-892.


6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Nguyen whose telephone number is 703-305-6209.

The examiner can normally be reached on Monday through Thursday 8 am-5pm..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on 703-305-4709. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-6606 for regular communications and 703-308-6606 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Kevin M. Nguyen
September 20, 2000


RICHARD A. HJERPE
SUPERVISORY PATENT EXAMINER
GROUP 2700